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1 Fault diagnosis in electronic circuits based on bilinear transformation in 3-D and 4-D spaces

Czaja, Z.; Zielonko, R.;

Instrumentation and Measurement, IEEE Transactions on , Volume: 52 , Issue: 1 , Feb. 2003
Pages:97 - 102

[\[Abstract\]](#) [\[PDF Full-Text \(546 KB\)\]](#) IEEE JNL

2 Using wavelet neural networks for the optimal design of electromagnetic devices

Wu Qing; Shen Xueqin; Yang Qingxin; Yan Weili;

Magnetics, IEEE Transactions on , Volume: 33 , Issue: 2 , March 1997
Pages:1928 - 1930

[\[Abstract\]](#) [\[PDF Full-Text \(328 KB\)\]](#) IEEE JNL

3 A MOS circuit for a nonmonotonic neural network with excellent retrieval capabilities

Asai, T.; Yokotsuka, H.; Fukai, T.;

Neural Networks, IEEE Transactions on , Volume: 7 , Issue: 1 , Jan. 1996
Pages:182 - 189

[\[Abstract\]](#) [\[PDF Full-Text \(712 KB\)\]](#) IEEE JNL

4 5th Biennial IEEE Conference on Electromagnetic Field Computation (CEFC)

Magnetics, IEEE Transactions on , Volume: 29 , Issue: 2 , Mar